



SF-6487

B. E. - II (Sem. IV) (ECC) Examination

May / June - 2011

Electronics Circuits

(Old Course)

Time : 3 Hours]

[Total Marks : 100

Instructions :

(1)

नीचे दर्शाविए निशानीवाणी विगतो उत्तरवही पर अवश्य कर्षवी. Fillup strictly the details of signs on your answer book.	Seat No. :
Name of the Examination :	<input type="text"/>
<input type="text" value="B. E. - 2 (SEM. 4) (ECC)"/>	<input type="text"/>
Name of the Subject :	<input type="text"/>
<input type="text" value="ELECTRONICS CIRCUITS (OLD)"/>	<input type="text"/>
Subject Code No. : <input type="text" value="6"/> <input type="text" value="4"/> <input type="text" value="8"/> <input type="text" value="7"/>	<input type="text"/>
Section No. (1, 2,.....) : <input type="text" value="NIL"/>	
	Student's Signature

- (2) Attempt all the questions.
- (3) Figure on right indicates full marks.
- (4) Assume suitable data wherever it is necessary.

1 (a) Attempt the following : 10

- (i) Draw the frequency response of RC coupled amplifier. Also mention the effect of different capacitances.
- (ii) Draw the symbol for the following :
 - (a) P channel Depletion type MOSFET
 - (b) N channel enhancement type MOSFET.
- (iii) What do you mean by amplification factor U ? Give its equation which relates transconductance factor g_m .
- (iv) Define Unity gain frequency. Also, relate it with f_B cut off frequency.
- (v) Mention the effect of bypass and coupling capacitors on frequency response of CE amplifier shortly.

(b) For the common source amplifier obtain the expression for voltage gain input and output impedance. 5

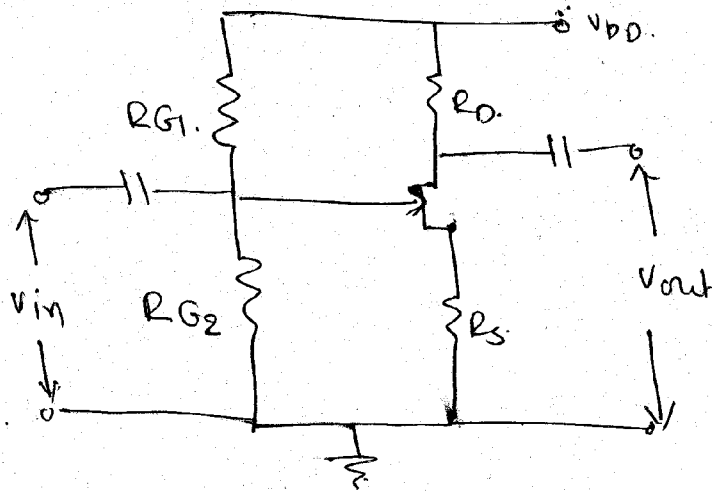
- (c) In an n-channel JFET biased by voltage divider method, determine the value of R_s to given operating point 5

$I_D = 4 \text{ mA}$ and $V_{DS} = 8 \text{ V}$

For the figure 1 shown below

$R_{G1} = 1.2 \text{ M}\Omega$; $R_{G2} = 0.6 \text{ M}\Omega$ JFET

Parameters are $I_{DSS} = 12 \text{ mA}$ and $V_p = -2 \text{ V}$.

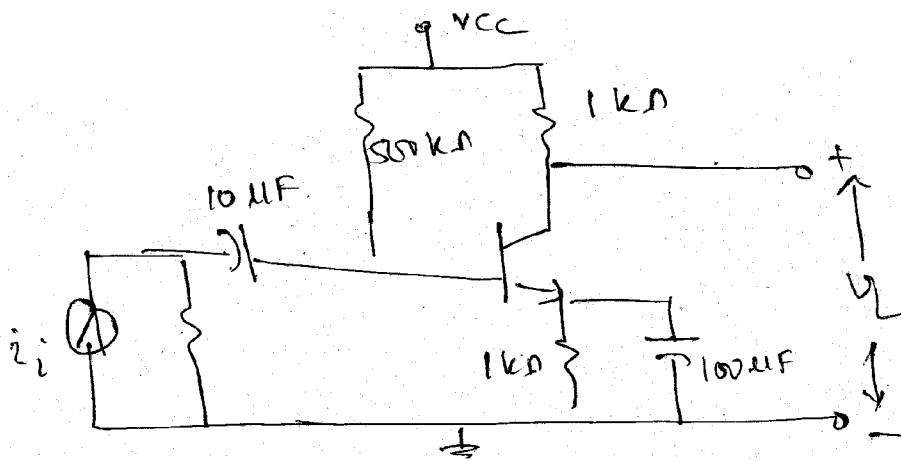


- 2 Attempt any two of the following : 16

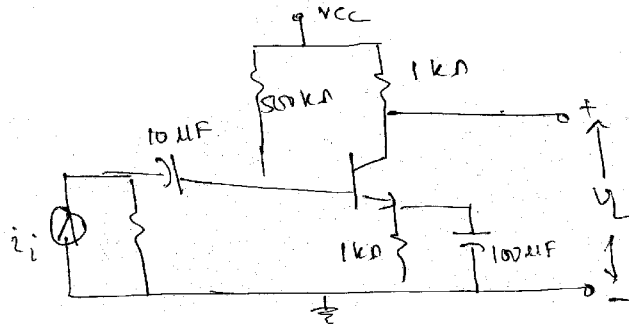
(a) Derive the expression for low frequency response for CE amplifier to find out the impact of coupling capacitor on lower BdB frequency. Find out the relationship between amplifier parameters and lower 3-dB frequency.

(b) Plot the magnitude and phase of $A_i = i_c/i_i$ as a function of the amplifier shown in figure 2.

$h_{fe} = 200$, $h_{ie} = 1 \text{ K}\Omega$



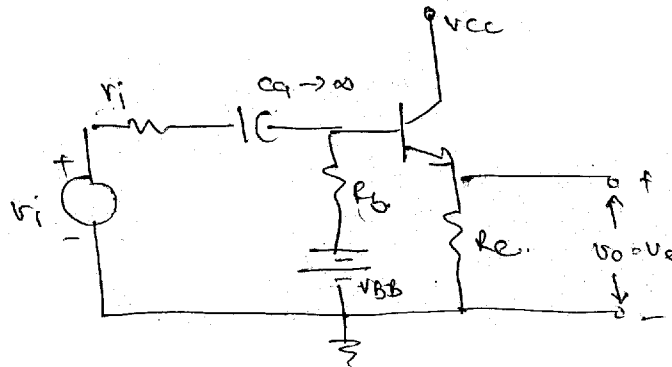
- (c) In figure 3 find transfer function v_o/v_{ii} plot the asymptotic magnitude and the phase, $h_{fe}=50$, $h_{ie}=500 \Omega$.



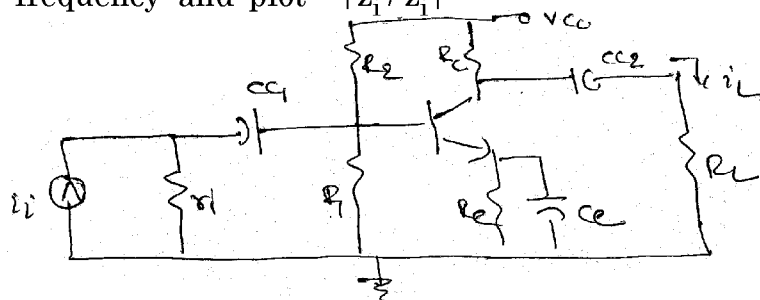
3 Attempt any two of the following :

14

- (a) Explain common base C(B) amplifier at high frequency and obtain the expression for current gain and α . Cut off frequency state the relation between f_{α} and f_B cut-off frequencies.
- (b) The emitter follower as shown in figure 4 has the component values
 $C_{b'e} = 1000 \text{ PF}$, $h_{Fe} = 100$ $C_{b'c} = 10 \text{ PF}$
 $R_e = 1 \text{ K}\Omega$, $r_{b'e} = 100 \Omega$, $R_i = 1 \text{ K}\Omega$
 Find the voltage gain A_v .



- (c) For figure 5; let $r_i = R_z = 10 \text{ K}\Omega$, $R_1=R_C=R_L=1\text{K}\Omega$, $R_e = 100 \Omega$ and $C_G = C_e=CC_2 = 200 \mu F$. The transistor is characterized by $\omega_T = 109 \text{ rad/S}$. $h_{fe} = 100$, $C_{b'c} = 5 \text{ PF}$ and $V_{bb}' = 0$, $7EQ = 10\text{m}\mu\Omega$. Find upper 3dB frequency and plot $|z_i/z_i|$



- 4 (a) (i) Derive equation for input resistance with feedback for voltage-series feedback. 5
- (ii) An amplifier with negative feedback has a voltage gain of 100. It is found that without feedback an input signal of 50 mV is required to produce a given output whereas with feedback, the input signal, must be 0.6V for the same output. Calculate the value of voltage gain without feedback and feedback ratio. 5
- (b) (i) The resonant circuit of a tuned collector transistor oscillator has a resonant frequency of 5MHz. If value of capacitance is increased by 50%, calculate the new resonant frequency. 5
- (ii) Explain the Barkhausen criterion. 5
- 5 (a) Write short note on the synchronously tuned amplifier. 7
- (b) Design a single tuned amplifier to operate at a center frequency of 455 kHz with a bandwidth of 10 kHz. The transistor has the parameters $g_m = 0.04 \text{ S}$, $h_{fe} = 100$, $C_{b'e} = 1000 \text{ PF}$ and $C_{b'c} = 10 \text{ PF}$. The bias network and the input resistance are adjusted so that $r_i = 5 \text{ K}\Omega$ and $R_L = 500 \Omega$. 8

OR

- 5 (a) Give advantages of negative feedback. 7
- (b) Draw and explain transistor phase-shift oscillator. 8
- 6 Write short note on any three : 15
- (i) Voltage-series feedback amplifier.
- (ii) Single tuned amplifier
- (iii) Wien bridge oscillator
- (iv) Crystal oscillator